


PATENT NUMBER

O.I.P.E. Q.2 SS' SCANNED Q.A. HC	PATENT DATE
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APPLICATION NO. 09/846272	CONT/PRIOR F	CLASS 257	SUBCLASS 900	ART UNIT 2811 2815	EXAMINER 
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Semiconductor device having capacitors for reducing power source noise

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div>		CLAIMS ALLOWED	
			<div>Total Claims</div>	<div>Print Claim for O.G.</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div> <div>_____ (Legal Instruments Examiner) _____ (Date)</div>		NOTICE OF ALLOWANCE MAILED	
			<div>_____</div>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <div>_____</div> <div>_____</div>	<div>_____ (Primary Examiner) _____ (Date)</div> <div>_____ (Legal Instruments Examiner) _____ (Date)</div>		ISSUE FEE	
			<div>Amount Due</div>	<div>Date Paid</div>
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<div>_____ (Legal Instruments Examiner) _____ (Date)</div>		ISSUE BATCH NUMBER	

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